Amendments to the Drawings:

The attached sheet of drawings includes changes to FIG. 3. This sheet, which includes FIG. 3, replaces the original sheet including FIG. 3.

Attachment: Replacement Sheet

REMARKS/ARGUMENTS

Provided herewith is a corrected drawing sheet for FIG. 3. Accordingly, the rejection to the drawings is overcome.

Amendments have been made to various claims to overcome the rejections under 35 U.S.C. §112, second paragraph, and thus it is respectfully submitted that these rejections are overcome.

Independent claims 1, 6 and 21 have been amended to recite that the methods of claims 1, 6 and 21 result in configuration of an unconfigured device. Accordingly, a tangible result is produced and these claims recite statutory subject matter. As to independent claim 18, a machine-readable storage medium is recited which stores instructions thereon for execution. Accordingly, statutory subject matter is recited.

Pending claims 1, 4, 6, 9-10, 12-13, 15-16, 18 and 20 stand rejected under 35 U.S.C. § 103(a) over BIOS and Kernel Developer's Guide (hereafter "Guide"). Applicants respectfully traverse the rejection. As to claim 1, the Guide nowhere teaches decoding a memory configuration access within a decoder of a processor to configure an integrated device in another processor. Instead, the Guide teaches the conventional sending of a configuration access to a host bridge for translation. Guide, p. 25. Certainly, there is no teaching or suggestion of the decoding of a memory configuration access within a decoder of a processor. Accordingly, claim 1 and the claims depending therefrom are over the Guide. For at least similar reasons, the remaining independent claims are patentable for the same failure to teach decoding a configuration access to a configuration cycle. For this same reason therefore, dependent claims that stand rejected under §103(a) over the Guide in view of a secondary reference is also overcome.
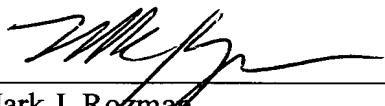
For at least the same reason, independent claim 21 is also patentable as the cited references nowhere teach or suggest decoding a memory configuration access within a processor to a configuration cycle and then routing the cycle from that processor to a chipset and from the chipset to another processor. Accordingly, claim 21 is also patentable.

In view of these remarks, the application is now in condition for allowance and the Examiner's prompt action in accordance therewith is respectfully requested. The Commissioner is authorized to charge any additional fees or credit any overpayment to Deposit Account No. 20-1504.

Respectfully submitted,

Date: 3/5/07

Mark J. Rozman
Registration No. 42,117
TROP, PRUNER & HU, P.C.
1616 S. Voss Road, Suite 750
Houston, Texas 77057-2631
(512) 418-9944 [Phone]
(713) 468-8883 [Fax]
Customer No.: 21906